

# PUNJAB TECHNICAL UNIVERSITY, JALANDHAR

Bachelor of Technology 3rd Semester (Re-appear) Examination held in May-2010  
 Result Notification No.PT/INDEX/754/

College: GNDEC, Ludhiana

## Electronics & Communication Engineering

| Sr No. | Roll No. Name                | A. Mathematics-III   |               | Network Ana. & Syn.  |               | Elec. Dev. & Ckt.    |               | Elec. Measurement & Instrumentation |               | Object Oriented Prog Using C++ |               | Lab-II: Elect. Devices & Networks |              | Instrumentation (LAB-II) |                  | Object Oriented Prog. LAB-III |              | Workshop Training     |               | Total x Credits / Re-appear(s)<br>Max Marks=750 | G.M Subject Code |            |
|--------|------------------------------|----------------------|---------------|----------------------|---------------|----------------------|---------------|-------------------------------------|---------------|--------------------------------|---------------|-----------------------------------|--------------|--------------------------|------------------|-------------------------------|--------------|-----------------------|---------------|---|------------------|------------|
|        |                              | AM-201 T2011 (40/16) | T201E (60/24) | EE-201 T2011 (40/16) | T201E (60/24) | EC-201 T2011 (40/16) | T201E (60/24) | EC-203 T203E (40/16)                | T203E (60/24) | CS-252 T252E (40/16)           | T252E (60/24) | EC-205 (P) P205E (30/12)          | P205E (20/8) | EC-207 (P) P207E (30/12) | (P) P207E (20/8) | CS-254 (P) P254E (30/12)      | P254E (20/8) | WSP (P) PWSIE (60/24) | PWSIE (40/16) |   |                  |            |
| 1      | Ravneet Singh<br>Dhian Singh | 6140405960           | 22            | 31                   | 24            | * 24                 | 22            | 28                                  | 20            | 27                             | 23            | 25                                | 22           | 12                       | 23               | 14                            | 19           | 12                    | 40            | 25  | 413              | 3<br>EE201 |

May be declared and Published Please.

AR (Exams)

By Controller (Exams)

Controller of Examinations

\* means 3 marks grace EE201 to complete the degree.

RT=Re-appear in both internal & external papers  
 RP=Re-appear in external papers only.  
 RTI=Re-appear in internal papers only.  
 "D" means Detained.  
 "A" means Absent.  
 "RL" means Result Late.

Prepared by: Pardooop Checked by: 1. Anita  
 2.